

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	A Dual-Loop PLL with DAC offset for Frequency Shift While Maintaining Input Tracking
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Application Number :

Confirmation Number:

First Named Applicant: Hide Hattori

Attorney Docket Number: PS-93

Art Unit:

Examiner:

Search string: (5546433 or 5546433).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	5546433	1996-08- 13	Tran	B1	375	376	
2	5351015	1994-09- 27	Masumoto	B1	331	001.R	
3	5247265	1993-09- 21	Norimatsu	B1	331	016	
4	5479073	1995-12- 26	Mamiya	B1	315	169.3	
5	4929918	1990-05- 29	Chung	B1	331	010	
6	4380742	1983-04- 19	Hart	B1	331	001.A	
7	4528523	1985-07- 09	Crowley	B1	331	010	
8	5978425	1999-11- 02	Takla	B1	375	374	
9	6198353	2001-03- 06	Janesch et al.	B1	331	016	

Signature

Examiner Name	Date
<i>Cassandra Cap</i>	<i>March 18, 2004</i>